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A New Prenexing Strategy for Quantified Boolean Formulae with Bi-Implications

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Abstract. Most of the recent and efficient decision procedures for quantified Boolean formulae accept formulae in negation normal form as input or in an even more restrictive format such conjunctive normal form. But real problems are rarely expressed in such forms. For instance, in specification, intermediate propositional symbols are used to capture local results with always the same pattern. So, in order to use most of the state-of-the-art solvers the original formula has firstly to be converted in prenex form. A drawback of this preliminary step is to destroy completely the original structures of the formula. Furthermore, during the prenexing process, bi-implications are translated in such a way that there is a duplication of their sub-formulae including the quantifiers. In general, this process leads to an exponential growth of the formula. In this work, we focus on this very common pattern of intermediate result. We introduce new logical equivalences allowing us to extract these sub-formulae in a way that can improve the performance of the state-of-the-art quantified Boolean solvers.

1 Introduction

The quantified Boolean formula (QBF) validity problem is a generalization of the Boolean formulae satisfiability problem. While the complexity of Boolean satisfiability problem is NP-complete, it is PSPACE-complete for the QBF validity problem. This is the price for a more concise representation of many classes of formulae. Many important problems in several research fields have polynomialtime translations to the QBF validity problem: AI planning [17,2], Bounded Model Construction [2], Formal Verification (see [6] for a survey).

Most of the recent and efficient decision procedures for QBF have formulae in negation normal form (NNF) as input or even more restrictive format such as formulae in conjunctive normal form (CNF). But problems are rarely expressed in such a form which destroyed completely their original structures. It is much more natural to use the full expressivity of the QBF language: all the usual connectors (including implication, bi-implication and xor) and quantifiers nested in the formula. Then translation schemas are required in order to use available QBF solvers. The NNF translation needs for QBF, as for first order logic, five steps: (i) replacement of the bi-implication and xor connectors by their definitions with implications, negations, conjunctions and/or disjunctions; (ii) renaming of propositional symbols such that distinct quantifiers bind occurrences of distinct propositional symbols; (iii) extraction of the quantifiers; (iv) replacement of the implications by their definitions with negations, conjunctions and/or disjunctions; (v) application of the DeMorgan's laws. The CNF translation adds one more step to the NNF translation. This latter translation has been largely studied [16, 10, 11] since this normal form is used as input of most of the decision procedure for the SAT problem. The three first steps of the above translation form the so-called prenexing translation. There is not a unique prenex QBF associated with a QBF and according to the chosen prenexing strategy, the computation time of the decision process is highly influenced [12]. As far as we know, there is no study for QBF about the extraction of quantifiers nested in the bi-implications. It is an important issue since the usual elimination of biimplications duplicates the nested quantifiers leading to an exponential growth of their numbers and the size of the formula.

The article is organized as follows: In Section 2, we present some preliminaries about propositional logic and quantified Boolean formulae. In Section 3, we analyse the links between prenexing and bi-implications, propose a methodology to extract the very common pattern of intermediate results and present one theoretical example and one practical example in order to evaluate our methodology. In Section 4, we report some experimental results for our methodology on our examples for state-of-the-art quanfitifed Boolean solvers. In Section 5, we conclude and draw some perspectives.

2 Preliminaries

2.1 Propositional logic.

The set of propositional symbols is denoted by \mathcal{PS} . Symbols \perp and \top are the propositional constants. Symbol \wedge stands for conjunction, \vee for disjunction, \neg for negation, \rightarrow for implication, \leftrightarrow for bi-implication and \oplus for xor. A literal is a propositional symbol or the negation of a propositional symbol. Definitions of the language of propositional formula **PROP** and semantics of all the Boolean symbols are defined in standard way. A formula is in negation normal form (NNF) if it is only constituted of conjunctions, disjunctions and literals. A formula is in conjunctive normal form (CNF) if it is a conjunction of disjunctions of literals. A substitution is a function from propositional symbols to **PROP**. This definition is extended as usual to a function from **PROP** to **PROP**: $[x \leftarrow F](G)$ is the formula obtained from G by replacing each occurrence of the propositional symbol x by the formula F. This definition is also extended as usual for the substitution of a formula by another formula. A valuation v is a function from \mathcal{PS} to {true, false}; the extension to **PROP** is denoted v^* . Propositional satisfaction is denoted $\models (v \models F \text{ means } v^*(F) = \mathbf{true}, \text{ the propositional formula})$ F is satisfied by v and v is a model of F). Logical equivalence is denoted \equiv .

2.2 Quantified Boolean Formulae.

The symbol \exists stands for the existential quantifier and \forall stands for the universal quantifier (q stands for any quantifier). The set QBF of quantified Boolean formulae is defined inductively as follows: if F is in **PROP** then it is also in **QBF**; if F is in **QBF** and x is a propositional symbol then $(\exists x F)$ and $(\forall x F)$ are also in **QBF**; if F is in **QBF** then $\neg F$ is also in **QBF**; if F and G are in **QBF** and \circ is in $\{\wedge, \lor, \rightarrow, \leftrightarrow, \oplus\}$ then $(F \circ G)$ is in **QBF**. The set of propositional symbols of a formula F is denoted $\mathcal{PS}(F)$. If a propositional symbol x is not under the scope of a quantifier (as in qx), then it is a free propositional symbol. The set of free propositional symbols of a QBF F is denoted $\mathcal{FPS}(F)$. A QBF is closed if its set of free propositional symbols is empty. A binder is a string $q_1 x_1 \dots q_n x_n$ with x_1, \ldots, x_n distinct propositional symbols and $q_1 \ldots q_n$ quantifiers. The empty string is denoted by ε . A QBF is in prenex form if it is constituted of a binder and a propositional formula called the matrix. A QBF is in conjunctive normal form if it is a prenex QBF and its matrix is in conjunctive normal form. The definition of substitution is extended to QBF as follows: $[x \leftarrow F](G)$ is the formula obtained from G by replacing *free* occurrences of the propositional symbol x by the formula F. The semantics of QBF is defined as follows: for every propositional symbol yand every QBF F

$$(\exists y \ F) \equiv ([y \leftarrow \top](F) \lor [y \leftarrow \bot](F))$$

and

$$(\forall y \ F) \equiv ([y \leftarrow \top](F) \land [y \leftarrow \bot](F)).$$

A QBF F is valid if $F \equiv \top$. The Boolean satisfiability (SAT) of an unquantified Boolean formula corresponds exactly to the decision problem of the validity of its existential closure. The complexity of the QBF validity problem is PSPACEcomplete while it is NP-complete for the SAT problem.

3 Prenexing and bi-implications

3.1 Motivations

In [12] are defined strategies for prenexing according to an order over the extraction of quantifiers. Some experiments have shown the high influence of this extraction order on the computation time for QBF solvers. However, there exists no rule for the extraction of quantifiers involved in a bi-implication (or xor). As pointed out in the introduction, the prenexing is decomposed in three steps that we detail more hereafter.

(i) The replacement of the bi-implication and xor connectors by their definitions with implications, negations, conjunctions and/or disjunctions is usually realized by the two following equivalences:

1)
$$(A \leftrightarrow B) \equiv ((A \to B) \land (B \to A))$$
 2) $(A \oplus B) \equiv ((A \lor B) \land \neg (A \land B))$

One can remark that the two QBF A and B are duplicated.

- (ii) The renaming of propositional symbols such that distinct quantifiers bind occurrences of distinct propositional symbols may be done before the extraction of quantifiers but may also be interleaved with this step.
- (iii) The extraction of quantifiers is usually based on the following classical first order equivalences which still hold for QBF $(F, G \text{ and } H \text{ are QBF} \text{ and } x \text{ is a propositional symbol such that } x \notin \mathcal{FPS}(H))$
 - $\begin{array}{lll} 3) & (\exists x \neg F) \equiv \neg (\forall x \ F) & 4) & (\forall x \neg F) \equiv \neg (\exists x \ F) \\ 5) & (\forall x \ F) \equiv F, \text{ if } x \notin \mathcal{FPS}(F) & 6) & (\exists x \ F) \equiv F, \text{ if } x \notin \mathcal{FPS}(F) \\ 7) & (\forall x \ (F \land H)) \equiv ((\forall x \ F) \land H) & 8) & (\forall x \ (F \lor H)) \equiv ((\forall x \ F) \lor H) \\ 9) & (\exists x \ (F \land H)) \equiv ((\exists x \ F) \land H) & 10) & (\exists x \ (F \lor H)) \equiv ((\exists x \ F) \lor H) \\ 11) & (\forall x \ (F \land G)) \equiv ((\forall x \ F) \land (\forall x \ G)) 12) & (\exists x \ (F \lor G)) \equiv ((\exists x \ F) \lor (\exists x \ G)) \\ 13) & (\forall x \ (H \to H)) \equiv ((\exists x \ F) \to H) & 14) & (\exists x \ (H \to H)) \equiv ((\forall x \ F) \to H) \\ 15) & (\forall x \ (H \to G)) \equiv (H \to (\forall x \ G)) & 16) & (\exists x \ (H \to G)) \equiv (H \to (\exists x \ G)) \end{array}$

Equivalences 13 to 16 for implication are easily deduced from equivalences 3 to 12. The extraction of quantifiers is a process which applies these equivalences from right to left until a fix-point corresponding to a prenex QBF is obtained. Prenexing preserves validity but does not guarantee the preservation of the size of the QBF nor the associated search space. Let us remark that the expressivity of the bi-implication connector exceeds the simple equivalence between $(A \leftrightarrow B)$ and $((A \rightarrow B) \land (B \rightarrow A))$: a propositional symbol associated to a quantifier in A or B is duplicated following the equivalences 1 and 13 to 16 in two propositional symbols: one associated to a universal quantifier and another associated to an existential quantifier, and so whatever is the original quantifier. If this has no impact w.r.t. the validity, it is not the case w.r.t. the computation process. We show in the following examples that the increase of the size of the formula is not the only one problem with prenexing, even with simple QBF.

Let a be a propositional symbol, ϕ , ϕ_1 and ϕ_2 three QBF such that $\phi = (\phi_1 \leftrightarrow (\exists a \ \phi_2))$ and $a \notin \mathcal{FPS}(\phi_1)$. By equivalence 1, the bi-implication is eliminated: $\phi \stackrel{1}{=} ((\phi_1 \rightarrow (\exists a \ \phi_2)) \land ((\exists a \ \phi_2) \rightarrow \phi_1))$. Formulae ϕ_1 and ϕ_2 have been duplicated. Then the quantifiers are extracted from the implications: $\phi \stackrel{16,13}{\stackrel{6}{=}} ((\exists a \ (\phi_1 \rightarrow \phi_2)) \land (\forall a \ (\phi_2 \rightarrow \phi_1)))$. Now, one occurrence of the propositional symbol a associated to a quantifier has to be renamed in order to extract the quantifiers from the conjunction. Let b be this new renaming propositional symbol and $\phi'_2 = [a \leftarrow b](\phi_2)$ then: $\phi \stackrel{9.7}{\stackrel{6}{=}} (\exists a \ (\forall b \ ((\phi_1 \rightarrow \phi_2) \land (\phi'_2 \rightarrow \phi_1))))$. The order of extraction of quantifiers may be reversed to obtain: $\phi \stackrel{7.9}{\stackrel{7.9}{=}} (\forall a \ (\exists b \ ((\phi_1 \rightarrow \phi_2) \land (\phi'_2 \rightarrow \phi_1)))))$. In the general case, if F is a QBF, $(\forall y \ (\exists x \ F))$ is not equivalent to $(\exists x \ (\forall y \ F))$. The possibility to permute these two quantifiers in this special case is a useful information which could be used by solvers which is not the case as far as we know. Moreover, propositional symbols a and b come from the same propositional symbol of the non prenex QBF but nothing in the prenex QBF keeps this information.

To conclude this motivation section, we consider how a quantifier has to be extracted from a sequence of bi-implications. We show in the following example that the choice of the position of the parenthesis can influence the size of the result of the prenexing process. Let a be a propositional symbol ϕ_r , ϕ_l , ϕ_1 , ϕ_2 and ϕ_3 some QBF such that $\phi_l = ((\phi_1 \leftrightarrow \phi_2) \leftrightarrow (\exists a \phi_3)), \phi_r = (\phi_1 \leftrightarrow (\phi_2 \leftrightarrow (\exists a \phi_3))))$ with $a \notin \mathcal{FPS}(\phi_1)$ and $a \notin \mathcal{FPS}(\phi_2)$ then $\phi_r \equiv \phi_l$ by associativity of the bi-implication. We compute a prenex form for ϕ_l :

$$\phi_l \stackrel{1}{\underset{=}{\overset{16,13,9,7}{\equiv}}} (((\phi_1 \leftrightarrow \phi_2) \to (\exists a \ \phi_3)) \land ((\exists a \ \phi_3) \to (\phi_1 \leftrightarrow \phi_2)))$$

$$\phi_l \stackrel{16,13,9,7}{\underset{=}{\overset{16,13,9,7}{\equiv}}} \exists a \forall b (((\phi_1 \leftrightarrow \phi_2) \to \phi_3) \land ([a \leftarrow b](\phi_3) \to (\phi_1 \leftrightarrow \phi_2)))$$

and then for ϕ_r :

$$\begin{array}{ll} \phi_r & \stackrel{1}{\equiv} & (\phi_1 \leftrightarrow ((\phi_2 \rightarrow (\exists a \ \phi_3)) \land ((\exists a \ \phi_3) \rightarrow \phi_2))) \\ \phi_r & \stackrel{1}{\equiv} & ((\phi_1 \rightarrow ((\phi_2 \rightarrow (\exists a \ \phi_3)) \land ((\exists a \ \phi_3) \rightarrow \phi_2))) \land \\ & (((\phi_2 \rightarrow (\exists a \ \phi_3)) \land ((\exists a \ \phi_3) \rightarrow \phi_2)) \rightarrow \phi_1)) \\ \phi_r & \stackrel{13,14,15,16,9,7}{\equiv} \\ \exists a \exists c \forall b \forall d \left((\phi_1 \rightarrow ((\phi_2 \rightarrow \phi_3) \land ([a \leftarrow b](\phi_3) \rightarrow \phi_2))) \land \\ & (((\phi_2 \rightarrow [a \leftarrow d](\phi_3)) \land ([a \leftarrow c](\phi_3) \rightarrow \phi_2)) \rightarrow \phi_1)) \end{array}$$

Formulae ϕ_l and ϕ_r , although equivalent, have different numbers of propositional symbols and different sizes after prenexing. If we consider the formula $(\phi_1 \leftrightarrow (\phi_2 \leftrightarrow \dots (\phi_n \leftrightarrow (\exists a \phi_{n+1}))))$, then 2^n propositional symbols are generated whose half of them are universally quantified. Formulae ϕ_n and ϕ_{n+1} are recopied 2^n times, formula ϕ_{n-1} is recopied 2^{n-1} times, and so on until ϕ_1 which is recopied two times. The size of the formula and the number of propositional symbols grow exponentially w.r.t. the number of crossed bi-implication. Any way, if every ϕ_k has a quantifier to extract, the worst case cannot be avoided.

3.2 Prenexing and intermediate results

Previous section shows how important is the issue of prenexing when quantifiers occur in the scope of bi-implications. Since $(A \oplus B) \equiv \neg (A \leftrightarrow B) \equiv (\neg A \leftrightarrow B)$, in what follows we only focus on bi-implication. This issue is, as far as we know, usually skipped in the process translating the specified problem in QBF to the equivalent CNF QBF given as input to a QBF solver. In this section, we focus on two very frequent cases which occur in the programming or specifying process: the declaration of intermediate results and the declaration of domain. The first case is based on the introduction of an existentially quantified propositional symbol in association with a conjunction in order to improve the readability of the specification or to capture the results of a calculus duplicated in many places. For QBF, by extension of a classical result of [18], we are interested in the pattern $(\exists x \ ((x \leftrightarrow F) \land G)), x \text{ being an intermediate propositional symbol not}$ occurring in F; it is easily proven from the semantics of the existential quantifier that this pattern is equivalent to $[x \leftarrow F](G)$. The latter case is based on the introduction of a universally quantified propositional symbol in association with an implication in order to express the domain of the propositional symbol (ie: a property to be verified by the symbol). In both cases, we call $(x \leftrightarrow F)$ the definition of x. The following result shows that these two techniques are actually the same.

Theorem 1. Let F and G be two QBF and x a propositional symbol which represents an intermediate result F, with $x \notin \mathcal{FPS}(F)$, then the following equivalence holds:

$$(\exists x \ ((x \leftrightarrow F) \land G)) \equiv (\forall x \ ((x \leftrightarrow F) \to G)).$$

The previous theorem is based on the following (propositional) equivalence:

 $((\neg A \lor B) \land (A \lor C) \land (B \lor C)) \equiv ((\neg A \lor B) \land (A \lor C)).$

By the previous theorem, we focus only on the existential pattern.

Since solvers usually have as input CNF QBF, the special status of the intermediate propositional symbols is completely lost and they are managed as the other propositional symbols of the problem. There is a simple way to get rid of these intermediate propositional symbols: we can simply apply the already shown equivalence $(\exists x \ ((x \leftrightarrow F) \land G)) \equiv [x \leftarrow F](G)$ but it can lead to an exponential growth of the size of the formula. Instead, we propose to extract these propositional symbols thanks to the following theorem.

Theorem 2. Let F, G and H be three QBF and x be a propositional symbol which represents the intermediate result F, with $x \notin \mathcal{FPS}(H)$, $x \notin \mathcal{FPS}(F)$ then

 $(H \leftrightarrow (\exists x \ ((x \leftrightarrow F) \land G))) \equiv (\exists x \ ((x \leftrightarrow F) \land (H \leftrightarrow G))).$

The Algorithm 1 realizes the recursive application of this theorem by calling the three functions described below.

```
Function 1 def search
In: A QBF F
In: A set of propositional symbols S_{ps}
Out: A set of definitions
  switch F do
   case (\exists x \ G)
   return def\_search(G, S_{ps} \cup \{x\})
   case (G \wedge H)
   return def_{search}(G, S_{ps}) \cup def_{search}(H, S_{ps})
   case (x \leftrightarrow G)
   if x \in S_{ps} then
     return \{(x \leftrightarrow G)\}
   else
     return Ø
   end if
   default
   return Ø
  end switch
```

Let $(\phi_H \leftrightarrow \phi)$ be a QBF, the def_search algorithm of Function 1 searches for all definitions matching $(\exists x \ ((x \leftrightarrow F) \land G))$ in ϕ with the constraint $x \notin \mathcal{FPS}(F)$ relaxed. In fact the pattern can be embedded in an alternation of similar patterns thanks to equivalences 9 and $(\exists x (\exists y F)) \equiv (\exists y (\exists x F))$ and associativity and commutativity of conjunction. For instance

 $def_search((\phi_1 \land (\exists a \ (\exists b \ ((a \leftrightarrow \phi_a) \land ((b \leftrightarrow \phi_b) \land \phi_2))))), \emptyset) = \{(a \leftrightarrow \phi_a), (b \leftrightarrow \phi_b)\}.$

In what follows, S_{def} denotes a set of definitions as $\{(a \leftrightarrow \phi_a), (b \leftrightarrow \phi_b)\}$ for instance.

Function 2 def_extract
In: A set of definitions S_{def}
In: A set of propositional symbols S_{ps}
Out: A list of definitions
$L_d := nil$
while there exists $(e \leftrightarrow d) \in S_{def}$ such that $\mathcal{FPS}(d) \subseteq S_{ps}$ do
$S_{def} := S_{def}$ without all the definitions on e
$S_{ps} := S_{ps} \cup \{e\}$
$L_d := \cdot ((e \leftrightarrow d), L_d)$
end while
$\mathbf{return} \ \ L_d$

The def_extract algorithm of Function 2 applies the constraint $x \notin \mathcal{FPS}(F)$ on the set of definitions obtained from the def_search algorithm thanks to a topological sort which extracts a list of definitions. Not all the definitions of the set are inserted in the list: for instance if $a \in \mathcal{FPS}(\phi_b)$, $b \in \mathcal{FPS}(\phi_a)$ and S_{ps} is a set of propositional symbols such that $a \notin S_{ps}$ nor $b \notin S_{ps}$ then $def_extract(S_{def}, S_{ps}) = nil$ otherwise if $a \notin \mathcal{FPS}(\phi_b)$ but $b \in \mathcal{FPS}(\phi_a)$ then $def_extract(S_{def}, S_{ps}) = \cdot((b \leftrightarrow \phi_b), \cdot((a \leftrightarrow \phi_a), nil))$ since

 $(\phi_1 \land (\exists a \ (\exists b \ ((a \leftrightarrow \phi_a) \land ((b \leftrightarrow \phi_b) \land \phi_2))))) \\ \equiv (\exists b \ ((b \leftrightarrow \phi_b) \land (\exists a \ ((a \leftrightarrow \phi_a) \land (\phi_1 \land \phi_2))))))$

The $def_applying$ algorithm of Function 3 actually applies Theorem 2 in two steps on the QBF ϕ for the list of definitions extracted by the $def_extract$ algorithm. Firstly, the definitions are replaced by \top in the QBF since the definitions are conjunctively connected and are reintroduced at the top of the QBF: for instance $(\phi_1 \land (\exists a \ (\exists b \ ((a \leftrightarrow \phi_a) \land ((b \leftrightarrow \phi_b) \land \phi_2))))))$ is replaced by

$$((b \leftrightarrow \phi_b) \land ((a \leftrightarrow \phi_a) \land (\phi_1 \land (\exists a \ (\exists b \ (\top \land (\top \land \phi_2))))))) \\ \equiv ((b \leftrightarrow \phi_b) \land ((a \leftrightarrow \phi_a) \land (\phi_1 \land (\exists a \ (\exists b \ \phi_2)))))).$$

Secondly, the existential quantifiers for the propositional symbols of the list of definitions are eliminated from the formula and reintroduced above it: for instance $((b \leftrightarrow \phi_b) \land ((a \leftrightarrow \phi_a) \land (\phi_1 \land (\exists a \ (\exists b \ \phi_2))))))$ is replaced by

$$(\exists b (\exists a ((b \leftrightarrow \phi_b) \land ((a \leftrightarrow \phi_a) \land (\phi_1 \land \phi_2))))) \\ \equiv (\exists b ((b \leftrightarrow \phi_b) \land (\exists a ((a \leftrightarrow \phi_a) \land (\phi_1 \land \phi_2))))).$$

```
Function 3 def_applying
In: A QBF F
In: A list of definitions L_d
Out: A QBF
  A list of definitions L_{save} := L_d
  while not empty(L_d) do
    (e \leftrightarrow d) := head(L_d)
    F := ((e \leftrightarrow d) \land [(e \leftrightarrow d) \leftarrow \top](F))
    L_d := tail(L_d)
   end while
   while not empty(L_{save}) do
    (e \leftrightarrow d) := head(L_{save})
    delete existential quantifier on e from F
    F := (\exists e \ F)
    L_{save} := tail(L_{save})
  end while
  return F
```

Algorithm 1 rec def extraction

In: A QBF FIn: A set of propositional symbols S_{ps} **Out:** An equivalent QBF to F with its definitions extracted switch F do case (qx G)**return** $(qx \ rec \ def \ extraction(G, \{x\} \cup S_{ps}))$ case $\neg G$ **return** $\neg rec_def_extraction(G, S_{ps})$ case $(G \circ H)$ and $\circ \in \{\land, \lor, \rightarrow\}$ **return** (rec def extraction(G, S_{ps}) \circ rec def extraction(H, S_{ps})) case $(G \leftrightarrow H)$ A QBF $G' := rec_def_extraction(G, S_{ps})$ A QBF $H' := rec_def_extraction(H, S_{ps})$ A list of definitions $L_d := def_search(G', \emptyset) \cup def_search(H', \emptyset)$ **return** $def_applying((G' \leftrightarrow H'), def_extract(L_d, S_{ps}))$ default return Fend switch

The $rec_def_extraction$ algorithm of Algorithm 1 applies recusively the extraction of the pattern on all possible definitions in an inside/out process. By this way some definitions can cross many equivalences.

It is not difficult to prove thanks to Theorem 2 the following correction theorem.

Theorem 3. Let F be a QBF then rec def extraction $(F, \emptyset) \equiv F$.

3.3 Bi-implication chains with intermediate results

We define a theoretical example in order to evaluate the impact of our methodology as a chain of bi-implications $th_n = (\xi_1 \leftrightarrow (\xi_2 \leftrightarrow ...(\xi_{n-1} \leftrightarrow (\xi_n))))$ in which each ξ_k contains an intermediate result as proposed in motivation section 3.1. We have chosen some ξ_k of the form $(\exists x_k((x_k \leftrightarrow (c \lor b)) \land (x_k \land a))))$, with x_k the intermediate result and a, b and c propositional symbols which are in some other links of the bi-implication chain. Formula th_n , for $n \ge 4$ is of the form:

$$\begin{split} th_n &= \exists e_0 \dots \exists e_{n-2} \forall u_0 \forall u_1 \forall u_2 \\ ((\exists x_n ((x_n \leftrightarrow (e_{n-4} \lor e_{n-3})) \land (x_n \land e_{n-2}))) \leftrightarrow \\ ((\exists x_{n-1} ((x_{n-1} \leftrightarrow (e_{n-5} \lor e_{n-4})) \land (x_{n-1} \land e_{n-3}))) \leftarrow \\ \dots \\ ((\exists x_4 ((x_4 \leftrightarrow (e_0 \lor e_1)) \land (x_4 \land e_2))) \leftrightarrow \\ ((\exists x_3 ((x_3 \leftrightarrow (u_2 \lor e_0)) \land (x_3 \land e_1))) \leftrightarrow \\ ((\exists x_2 ((x_2 \leftrightarrow (u_1 \lor u_2)) \land (x_2 \land e_0))) \leftrightarrow \\ (\exists x_1 ((x_1 \leftrightarrow (u_0 \lor u_1)) \land (x_1 \land u_2)))))) \dots)) \end{split}$$

For all n, th_n is not valid.

The nesting of quantifiers and bi-implications leads to an exponential explosion of the size of th_n in CNF w.r.t. n which is not the case with our methodology. For instance, with n = 7, the CNF converted QBF has 1148 propositional symbols (98 are universally quantified) and 3038 clauses whereas the QBF obtained by application of the $rec_def_extraction$ algorithm and then converted into CNF has only 33 propositional symbols (3 are universally quantified) and 82 clauses. For n = 18, the CNF converted QBF has not been decided by any of the state-of-the-art solvers in one hour whereas for n = 6000 the QBF obtained by application of the $rec_def_extraction$ algorithm and then converted into CNF has been decided in less than one hour.

3.4 Hardware verification: the *n*-bit ripple-carry adder

We also evaluate our methodology on a more simple (with only one bi-implication crossed by some definitions) and classical example of hardware verification: the *n*-bit ripple-carry adder. We follow the approach of bounded model construction [1] which is a method for generating models for a monadic formula by reducing its satisfiability problem to a QBF validity problem [3,2]. The example is about a parametrized family of ripple-carry *n*-bits adders. For a given number *n*, one has to verify the equivalence of the digital circuit with its specification. Hereafter, the QBF add_{impl} represents the implementation of the adder and add_{spec} the specification, (*n* represents the size of the ripple-carry adder, *A* and *B* represent *n*-bit input vectors, *S* represents the *n*-bit output and the Booleans c_i and c_o are the carry-in and carry-out respectively). By this way, the validity of the following QBF add_n ensures the correctness of the physical *n*-bit ripple-carry adder.

$$add_n = \forall A \forall B \forall S \forall c_i \forall c_o (add_{impl}(n, A, B, S, c_i, c_o) \leftrightarrow add_{spec}(n, A, B, S, c_i, c_o))$$

We give an example for the *n*-bit adder with n = 1: QBF

$$\begin{aligned} add_1 = \forall A_0 \forall B_0 \forall S_0 \forall c_i \forall c_o \; (add_{impl}(1, A_0, B_0, S_0, c_i, c_o) \\ & \leftrightarrow add_{spec}(1, A_0, B_0, S_0, c_i, c_o)) \end{aligned}$$

with

$$\begin{aligned} add_{impl}(1, A_0, B_0, S_0, c_i, c_o) &= [\exists x_1 \exists x_2 ((D_1(x_1) \land D_2(x_2)) \land (\exists x_3 \exists x_4 \exists x_5 \\ (D_3(x_3) \land C_1(x_3, x_1) \land D_4(x_4) \land \\ D_5(x_5, x_1, x_3) \land C_2(x_2, x_5, x_4)))))] \\ add_{spec}(1, A_0, B_0, S_0, c_i, c_o) &= [\exists x_6 \exists x_7 (D_1(x_6) \land D_2(x_7) \land C_3(x_6, x_7) \\ \land C_4(x_6))] \end{aligned}$$

encodes the check of the adder for n = 1 with the following Boolean functions:

$$D_{1}(x) = (c_{i} \leftrightarrow x),$$

$$D_{2}(x) = (c_{o} \leftrightarrow x),$$

$$D_{3}(x) = (x \leftrightarrow (A_{0} \oplus B_{0})),$$

$$D_{4}(x) = (x \leftrightarrow (A_{0} \wedge B_{0})),$$

$$D_{5}(x, y, z) = (x \leftrightarrow (y \wedge z)),$$

$$C_{1}(x, y) = (S_{0} \leftrightarrow (x \oplus y)),$$

$$C_{2}(x, y, z) = (x \leftrightarrow (y \lor z)),$$

$$C_{3}(x, y) = ((((A_{0} \land B_{0}) \lor (A_{0} \land x)) \lor (B_{0} \land x)) \leftrightarrow y),$$

$$C_{4}(x) = (((A_{0} \leftrightarrow B_{0}) \leftrightarrow S_{0}) \leftrightarrow x).$$

Functions C_k , $1 \le k \le 4$, represent the core of the implementation and specification of the *n*-bit ripple-carry adder. Formulae add_{impl} and add_{spec} contain instances of functions D_k , $1 \le k \le 5$, which are definitions that we will extract.

The hereafter QBF add'_1 is the initial QBF add_1 translated by the classical prenexing algorithm (propositional symbols y_k come from the x_k by recopy of the two subformulae of the bi-implication). Universal quantifiers are first extracted from both parts of the bi-implication in order to minimize the alternation of quantifiers.

```
\begin{aligned} add'_{1} &= \forall A_{0} \forall B_{0} \forall S_{0} \forall c_{i} \forall c_{o} \forall y_{1} \forall y_{2} \forall y_{3} \forall y_{4} \forall y_{5} \forall y_{6} \forall y_{7} \exists x_{1} \exists x_{2} \exists x_{3} \exists x_{4} \exists x_{5} \exists x_{6} \exists x_{7} \\ & [[(D_{1}(y_{1}) \land D_{2}(y_{2}) \land D_{3}(y_{3}) \land C_{1}(y_{3}, y_{1}) \land D_{4}(y_{4}) \land D_{5}(y_{5}, y_{1}, y_{3}) \land C_{2}(y_{2}, y_{5}, y_{4})) \\ &\rightarrow (D_{1}(x_{6}) \land D_{2}(x_{7}) \land C_{3}(x_{6}, x_{7}) \land C_{4}(x_{6}))] \land \\ & [(D_{1}(y_{6}) \land D_{2}(y_{7}) \land C_{3}(y_{6}, y_{7}) \land C_{4}(y_{6})) \\ &\rightarrow (D_{1}(x_{1}) \land D_{2}(x_{2}) \land D_{3}(x_{3}) \land C_{1}(x_{3}, x_{1}) \land D_{4}(x_{4}) \land D_{5}(x_{5}, x_{1}, x_{3}) \land C_{2}(x_{2}, x_{5}, x_{4}))]] \end{aligned}
```

The order of the quantifiers highly influences the efficiency of the solvers [12]. During the prenexing process of add_1 into add'_1 , the order of the propositional symbols is only constrained by the partial order induced by the fact that the universal quantifiers of the initial propositional symbols have to precede the existential quantifiers.

The hereafter QBF add_1^t is the initial QBF add_1 translated by our algorithm $rec_def_extraction$ and then translated into a prenex form according to the classical algorithm.

 $\begin{aligned} add_{1}^{t} &= \forall A_{0} \forall B_{0} \forall S_{0} \forall c_{i} \forall c_{o} \exists x_{1} \exists x_{2} \exists x_{3} \exists x_{4} \exists x_{5} \exists x_{6} \exists x_{7} \\ & [D_{1}(x_{1}) \land D_{2}(x_{2}) \land D_{3}(x_{3}) \land D_{4}(x_{4}) \land D_{5}(x_{5}, x_{1}, x_{3}) \land D_{1}(x_{6}) \land D_{2}(x_{7})] \land \\ & [[C_{1}(x_{3}, x_{1}) \land C_{2}(x_{2}, x_{5}, x_{4})] \leftrightarrow [C_{3}(x_{6}, x_{7}) \land C_{4}(x_{6})]] \end{aligned}$

The obtained matrix of the QBF is composed of two parts: the definition of the intermediate propositional symbols followed by the core of the equivalence between the implementation and the specification of the adder which uses these intermediate propositional symbols.

4 Experimental Results

In order to evaluate the impact of our new methodology, we have developed a Prolog program to generate instances of chains of bi-implications with intermediate results th_n as described in subsection 3.3 and instances of the *n*-bit ripple-carry adder add_n as described in subsection 3.4. The applied prenexing process does not search for an optimal order of the quantifiers. The order of the definitions in the matrix has not been optimized either. The CNF conversion is computed thanks to the introduction of intermediate existentially quantified propositional symbols. This CNF conversion only polynomially increases the size of the QBF. The experiments have been realized on an Intel(R) Xeon(R) (2.83 GHz) with 4GB of memory. For our experiments we used the following QBF solvers:

- sKizzo v0.8.2-beta [5] which is based on symbolic skolemization [4];
- Quantor 3.0 [7] which combines Q-resolution [15], to eliminate an innermost existential quantifier, and expansion, to eliminate an innermost universal quantifier;
- QuBE-BJ1.2 [14] which extends to QBF the Davis-Logemann-Loveland [9] procedure with backjumping and two more recent versions, QuBE6.5 (resp. QuBE6.1), which integrates (resp. does not integrate) as preprocessing the Q-resolution [8];
- qpro [13] which is a decision procedure for non prenex QBF in negation normal form (i.e. QBF only with conjunction, disjunction, literals and quantifiers) based on a sequent calculus for QBF, the GQBF calculus.

Experiments have been done firstly with the default settings of the different solvers (in particular, sKizzo is set with the Q-resolution preprocessing).

4.1 Chains of bi-implications with intermediate results

Figures 1 and 2 report computation time in seconds for th_n the chains of biimplications with or without our methodology and converted in prenex CNF for sKizzo, QuBE 6.5 and Quantor and in non prenex NNF for qpro. Actually, the size of the QBF converted in CNF or NNF grows exponentially with n. **Fig. 1.** Computation time in seconds for th_n the chains of bi-implications for QuBE 6.5 with and without intermediate results translated by $rec_def_extraction$ algorithm and converted in prenex CNF and for qpro with and without intermediate results translated by $rec_def_extraction$ algorithm and converted in non prenex NNF.

Fig. 2. Computation time in seconds for th_n the chains of bi-implications with intermediate results translated by $rec_def_extraction$ algorithm and converted in prenex CNF for sKizzo, QuBE 6.5 and Quantor and in non prenex NNF for qpro.

None of the tested QBF solvers has succeeded in deciding if th_n is valid or not for n > 17 without translation by our rec def extraction algorithm (QuBE6.5 succeeds in 2657 seconds for n = 15 but computation time exceeded the 3600 seconds for n = 16, Quantor succeeds in 0.1 second for n = 5 but computation space exceeded the available memory space for n = 6, sKizzo succeeds in 31.9 seconds for n = 7 but computation space exceeded the available memory space for n = 8, approduced succeeds in 1145.8 seconds for n = 17 but computation time exceeded the 3600 seconds for n = 18). Due to the lack of space and since sKizzo without our methodology failed to decide for n = 8 and succeeds to decide with our methodology for n = 2000, we do not report more results about sKizzo with or without our methodology. For the same reason, since Quantor failed to decide for n = 5 and succeeds to decide with our methodology for n = 6000, we do not report more results about Quantor with or without our methodology. Figure 1 reports the results for QuBE 6.5 and qpro with or without our methodology. Computation time axis is in log scale. Figure 2 reports the results for the tested QBF solvers on the QBF translated by our rec def extraction algorithm (and then converted in prenex CNF for sKizzo, QuBE and Quantor and non prenex NNF for qpro). The axes are both in log scale.

4.2 The *n*-bit ripple-carry adder

Results of the evaluation for the *n*-bit ripple-carry adder are summarized in Table 1, computation times are in seconds, T (for "time out") means that the computation time exceeded 3600 seconds, M means that the computation space exceeded the available memory space. These results show that the translated QBF have almost always the best results. For all the solvers but sKizzo the results are clear. For sKizzo, with n < 12 the improvement is substantial, but speedup decreases for ripple-carry adder with larger size. The result of the translation penalizes sKizzo on the largest instances: it may be observed thanks to the trace that sKizzo, by the Q-resolution preprocessing, removes some intermediate results (canceling part of our translation). Since QuBE6.5, which integrates a Q-resolution preprocessing, has results worse than those obtained by QuBE-BJ1.2, we think that Q-resolution has a great impact on this problem. In Table 1 are

	sKizzo with		sKizzo without		QuBE						
	Q-resolution		Q-resolution		6.5	6.1 BJ1.2		Quantor		qpro	
n	without	with	without	with	with	with	with	without	with	without	with
4	0,1	0,1	0,2	0,1	0,7	1,3	0,1	5,3	0,1	1,3	1,5
5	0,2	0,1	0,2	0,1	6,7	79,5	1,1	75,2	0,2	21,2	17,3
6	0,4	0,1	1,3	0,1	61,4	Т	9,8	T	1,8	335,9	186,1
7	1,0	0,1	1,3	0,1	561,0	Т	83,8	M	11,1	T	1967,2
8	2,0	0,2	3,3	0,1	T	Т	714,5	M	106,0	T	Т
9	3,6	0,4	1,5	0,1	T	Т	Т	M	2936, 6	T	Т
10	7,5	1,6	3,2	0,1	T	Т	Т	M	Μ	T	Т
11	$_{32,0}$	6,7	5,4	0,1	T	Т	Т	M	Μ	T	Т
12	14,1	11,9	4,8	0,1	Т	Т	Т	Μ	Μ	Т	Т
13	13,6	16,1	3,6	0,2	T	Т	Т	M	Μ	T	Т
14	16,3	15,6	5,3	0,3	T	Т	Т	M	Μ	T	Т
15	19,6	21,9	4,7	0,3	T	Т	Т	M	Μ	Т	Т
16	33,5	29,3	6,5	0,3	Т	Т	Т	Μ	Μ	Т	Т
17	22,6	15,0	17,0	0,4	Т	Т	Т	Μ	Μ	Т	Т
18	19,3	14,4	14,0	0,5	T	Т	Т	M	Μ	Т	Т
19	32,4	26,8	23,4	0,5	T	Т	Т	M	Μ	Т	Т
20	82,5	21,7	28,9	0,4	Т	Т	Т	Μ	Μ	Т	Т
21	$46,\! 6$	20,7	26,1	1,0	T	Т	Т	M	Μ	T	Т
22	35,7	27,9	19,6	1,0	T	Т	Т	M	Μ	T	Т
23	37,7	14,0	294,9	0,8	Т	Т	Т	M	Μ	T	Т

Table 1. Computation time in seconds for the *n*-bit ripple-carry adder with intermediate results translated by $rec_def_extraction$ algorithm and converted in prenex CNF for sKizzo, QuBE and Quantor and in non prenex NNF for qpro.

also reported the experiments for sKizzo, but without Q-resolution, for which we obtain the best results of our experiments. The improvement provided by our new translation is very substantial.

5 Conclusion

In this work, we proposed different logical equivalences allowing to deal with intermediate results involved in bi-implications or xor inside a non prenex QBF. One consequence of the use of these equivalences is to keep the size of the original QBF, and its number of propositional symbols, during a prenexing process to extract internal quantifiers. It is also possible to choose the nature of the quantifier of the intermediate propositional symbol and then to reduce the number of alternations of quantifiers.

To evaluate the practical impact of our present work, we realized two kinds of experiments. Firstly, we used an artificial example exhibited in our motivation section to illustrate that in some cases the usual prenexing process leads to a QBF with an exponential size. Our experimental results show that our new prenexing strategy avoids this exponential growing of the prenex formula. So, an immediate consequence of our contribution is that available QBF solvers are now able to deal with such large QBF, when they were unable to solve very small ones. Secondly, we chose a practical and realistic example: the formal verification of a digital circuit that has to conform to a given specification. The results show the positive impact of our new prenexing strategy. It is worth noting that if Qresolution is used by the solver then the efficiency decreases. So, one interesting topic of research may be to study if the efficiency of Q-resolution can be restored if it is not applied on intermediate results.

In our future works, we plan to extend our results to the general case of quantifiers involved in bi-implications or xor, and not only for intermediate results. Another improvement of our present work would be to compare different variants of our prenexing strategy since our experimental results show that the nature of the chosen quantifier may importantly influence the resolution time. Moreover, having observed that procedures inside QBF solvers use, more and more often, a structure of quantifiers and a non prenex clausal form of QBF, it would be interesting to work directly on the original (non prenex non CNF) encoding of the problem to solve. So, we plan to study how to translate a non prenex non CNF QBF in a non prenex CNF QBF, to sort the propositional symbols in different categories: problem propositional symbols, intermediate results and propositional symbols only dedicated to the encoding. Then, we would be able to exploit more information and to evaluate the impact of heuristics, like Q-resolution, on these different kinds of propositional symbols.

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